



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Hisashi Ohtani
Serial No. : 09/596,755
Filed : June 15, 2000
Title : MANUFACTURING METHOD OF SEMICONDUCTOR AND
MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE

Art Unit : 1765
Examiner : Robert Kunemund

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT IN REPLY TO ACTION OF JUNE 23, 2003

Please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 10 of this paper.